

Brief Bio of Bharath Ram

• Palo Alto , USA
bharathram2011@gmail.com



Bharath Ram is currently a Charter Member, Angel Investor and Chair for Sponsorships at Silicon Valley chapter of The Indus Entrepreneurs (TiE). Bharath was formerly VP of global business development heading up international business for Verisign Inc., where he also provided corporate oversight in the Board of Directors of Verisign Japan, a publicly listed subsidiary of Verisign Inc.

Earlier Bharath has held management positions at LSI Corporation, VM Labs Inc. (acquired by Genesis Microchip Inc. and now part of ST Microelectronics), RSA Security (division of EMC Corporation) and IBM Corporation. Bharath has graduated with Bachelors in Electrical Engineering from The National Institute of Technology (India), Masters in Business Administration (MBA) from The University of Hawaii (USA) alongside a concurrent Advanced Diploma in Japanese Language Studies and Cross Cultural Management Studies from the Japan America Institute of Management Sciences, Honolulu (USA). Bharath is fluent in Japanese and multiple Asian languages with over 15 years of global professional experience in starting up and scaling the overseas operations of multiple US technology companies. Bharath was a finalist for Rhodes scholarship and granted the prestigious Fujitsu Asia Pacific Scholarship as recipient-scholar representing India in 1993.